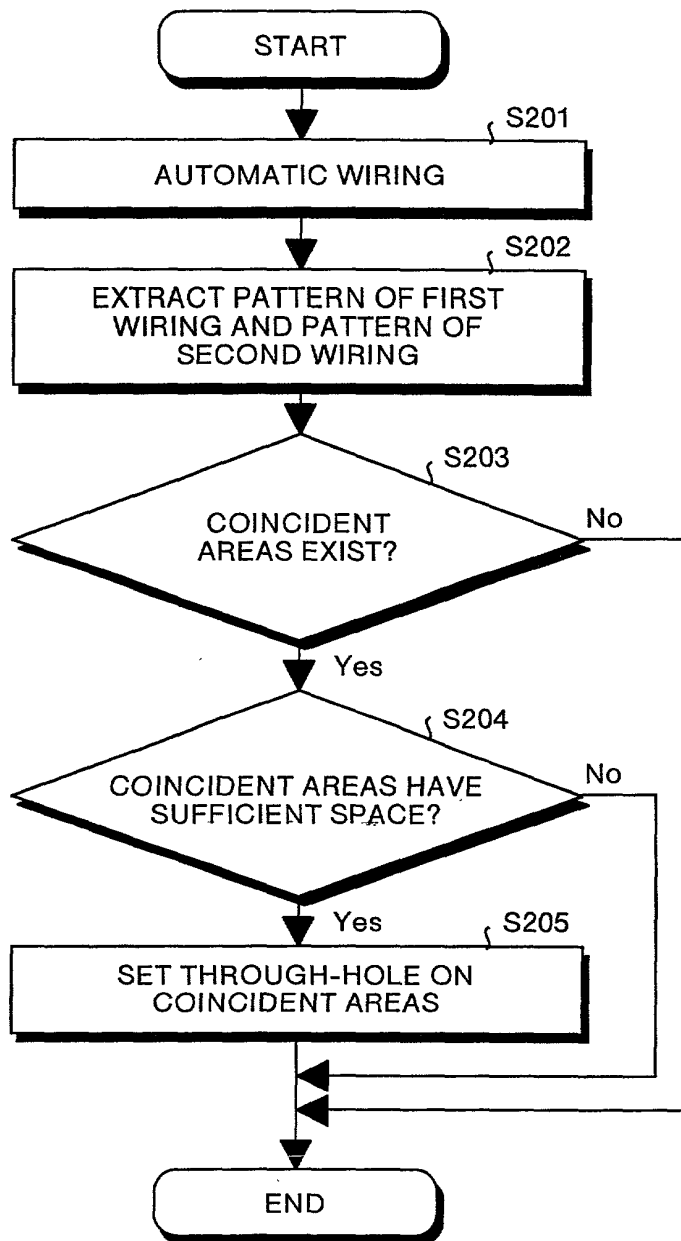


FIG.2



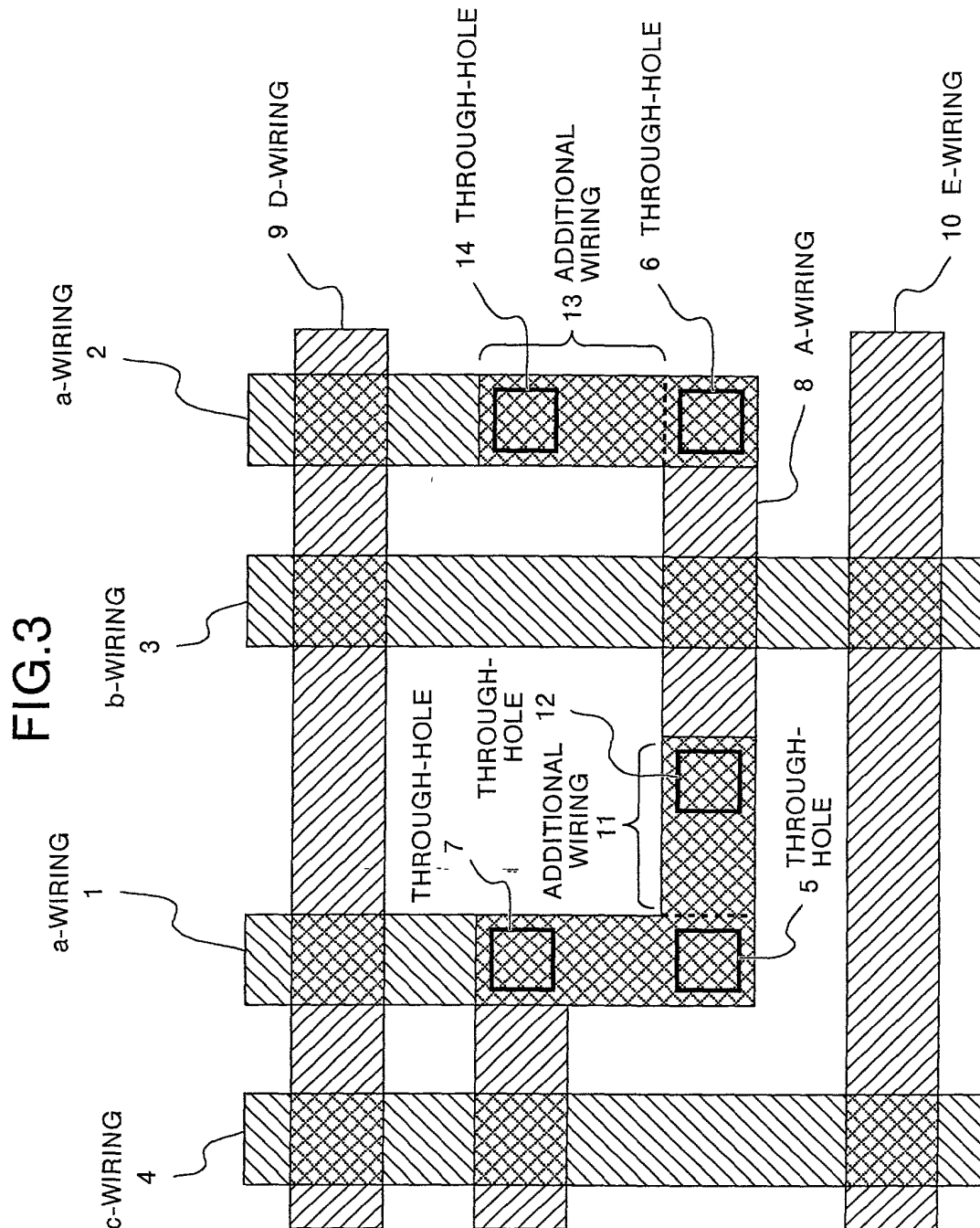
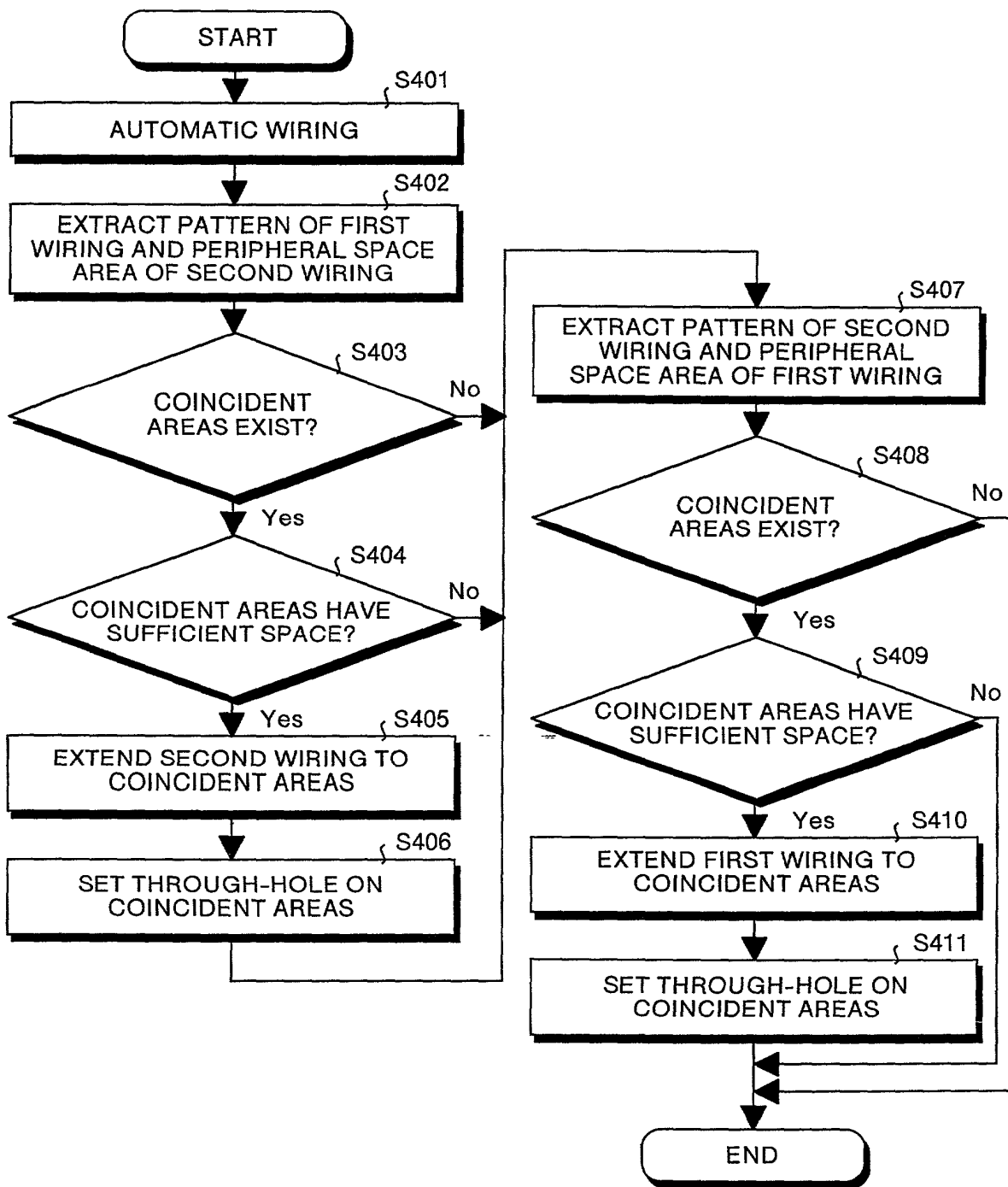
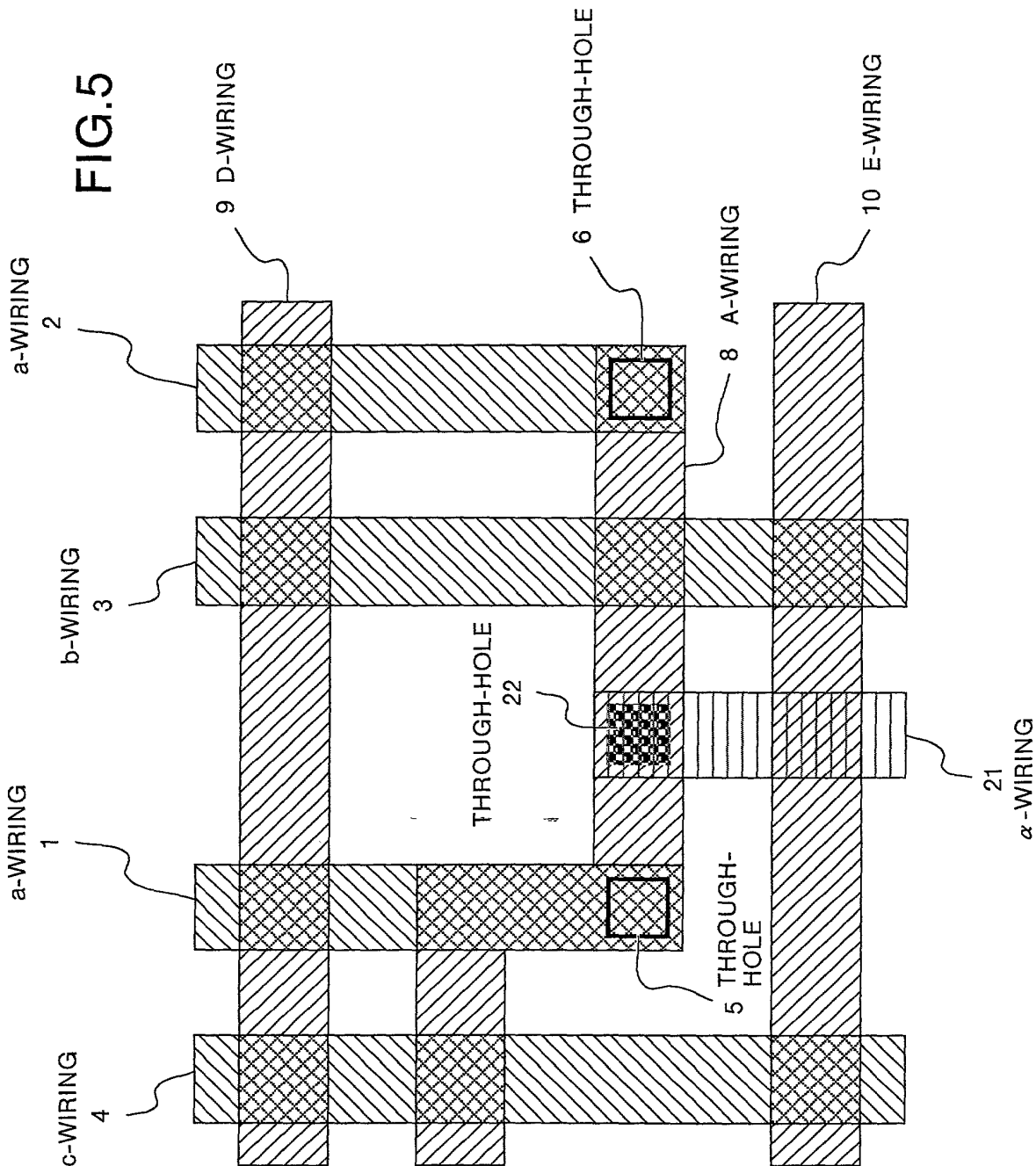
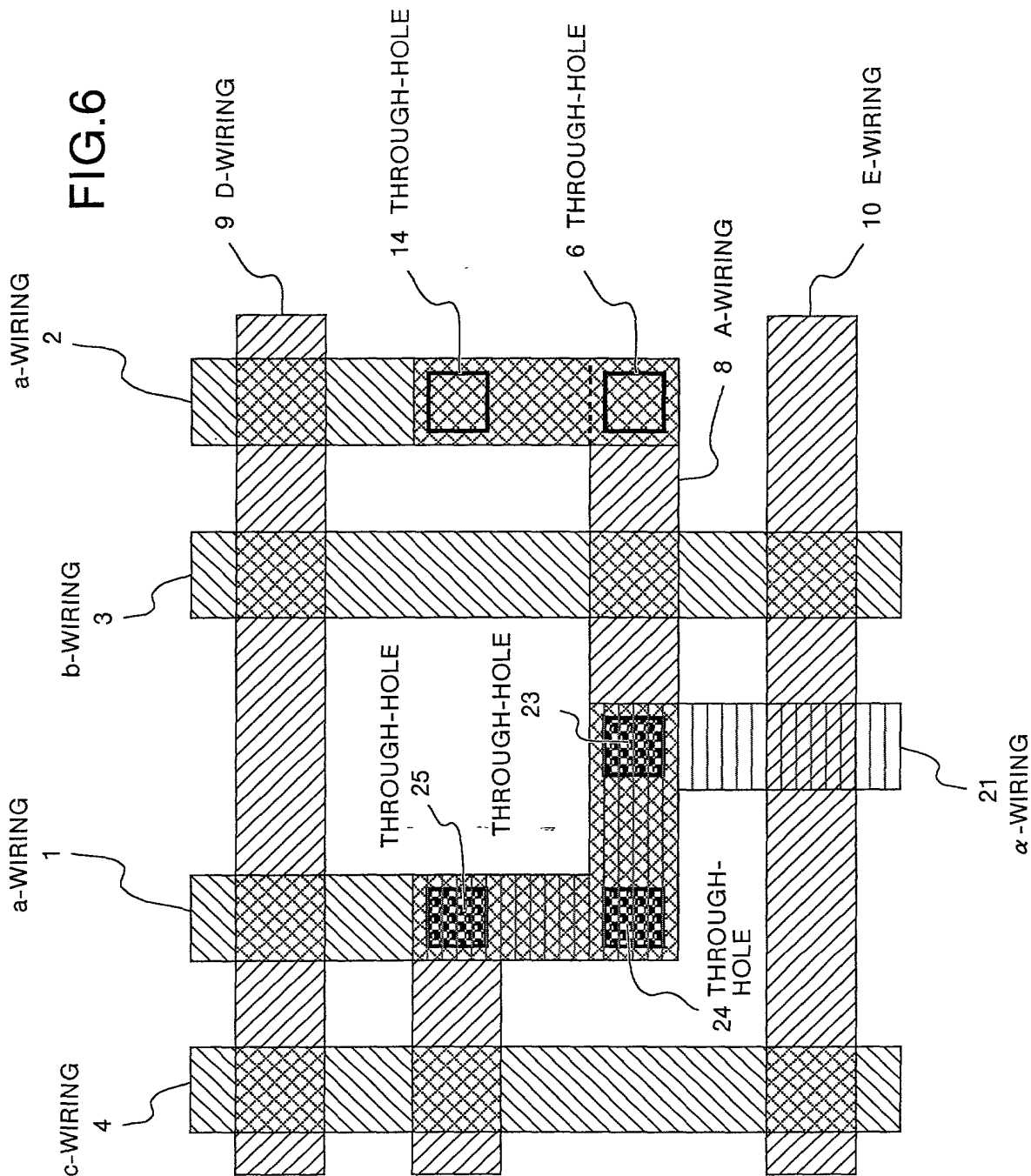


FIG.4







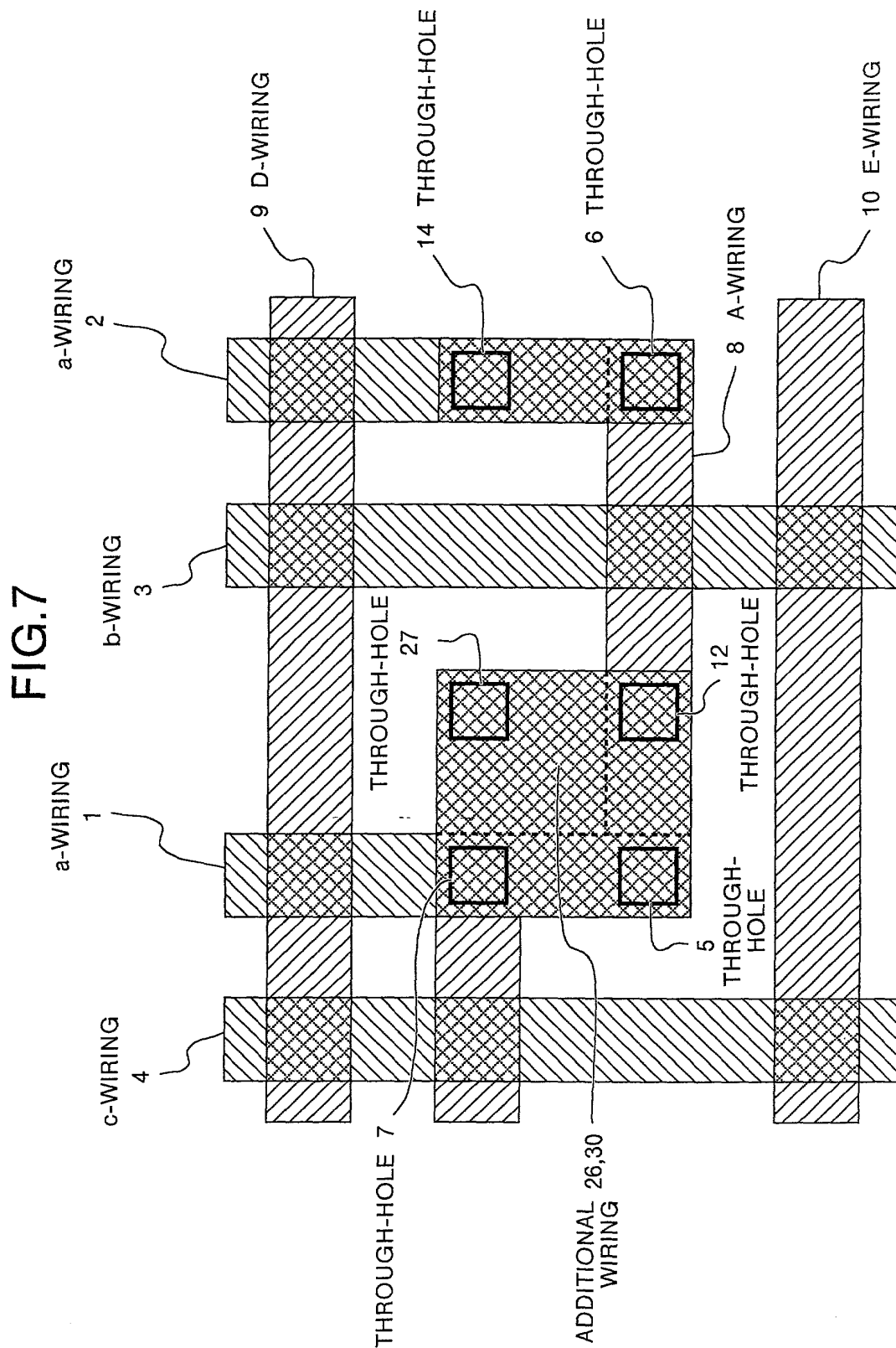
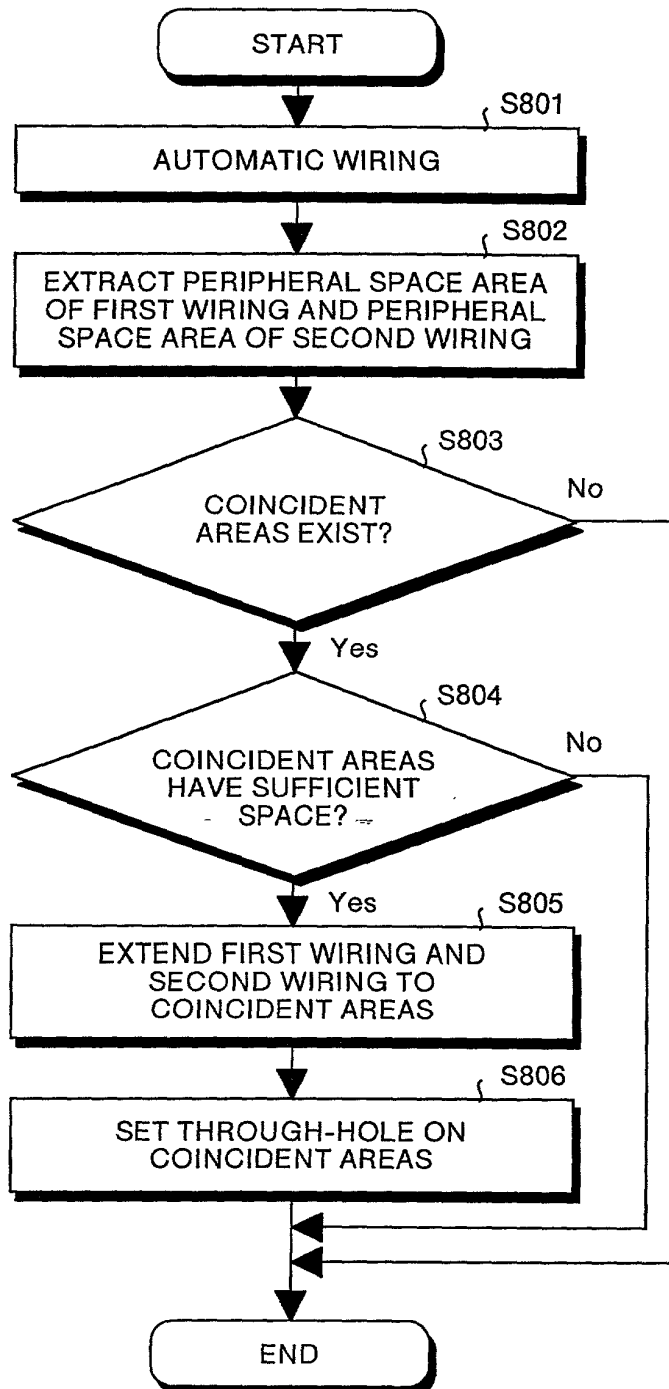


FIG.8





Title: METHOD OF WIRING SEMICONDUCTOR INTEGRATED CIRCUIT, SEMICONDUCTOR  
INTEGRATED CIRCUIT, AND COMPUTER PRODUCT

Inventors: Takao HASEGAWA

Atty Docket No.: 401558

Leydig, Voit & Mayer, Ltd.

202-737-6770

9/11

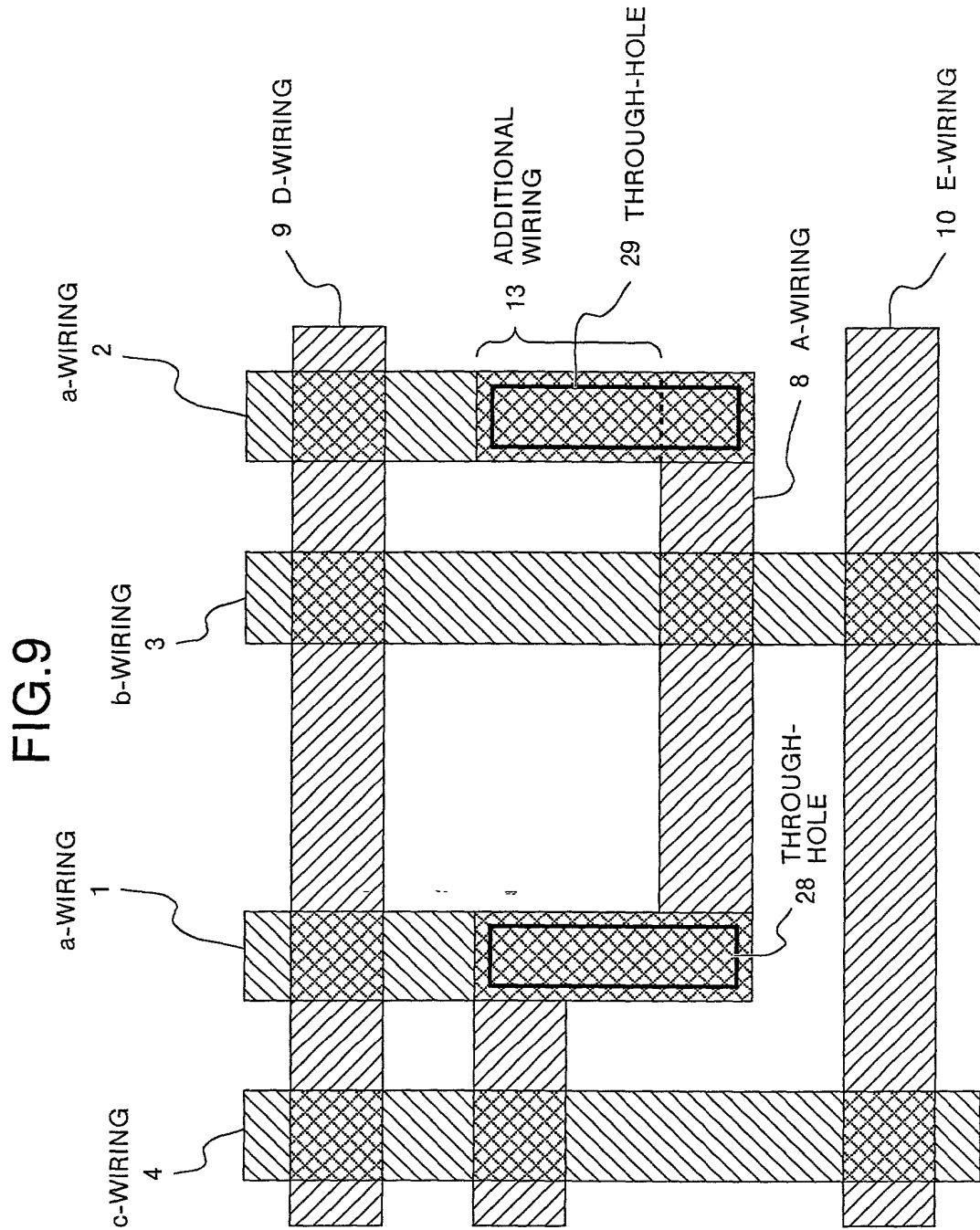
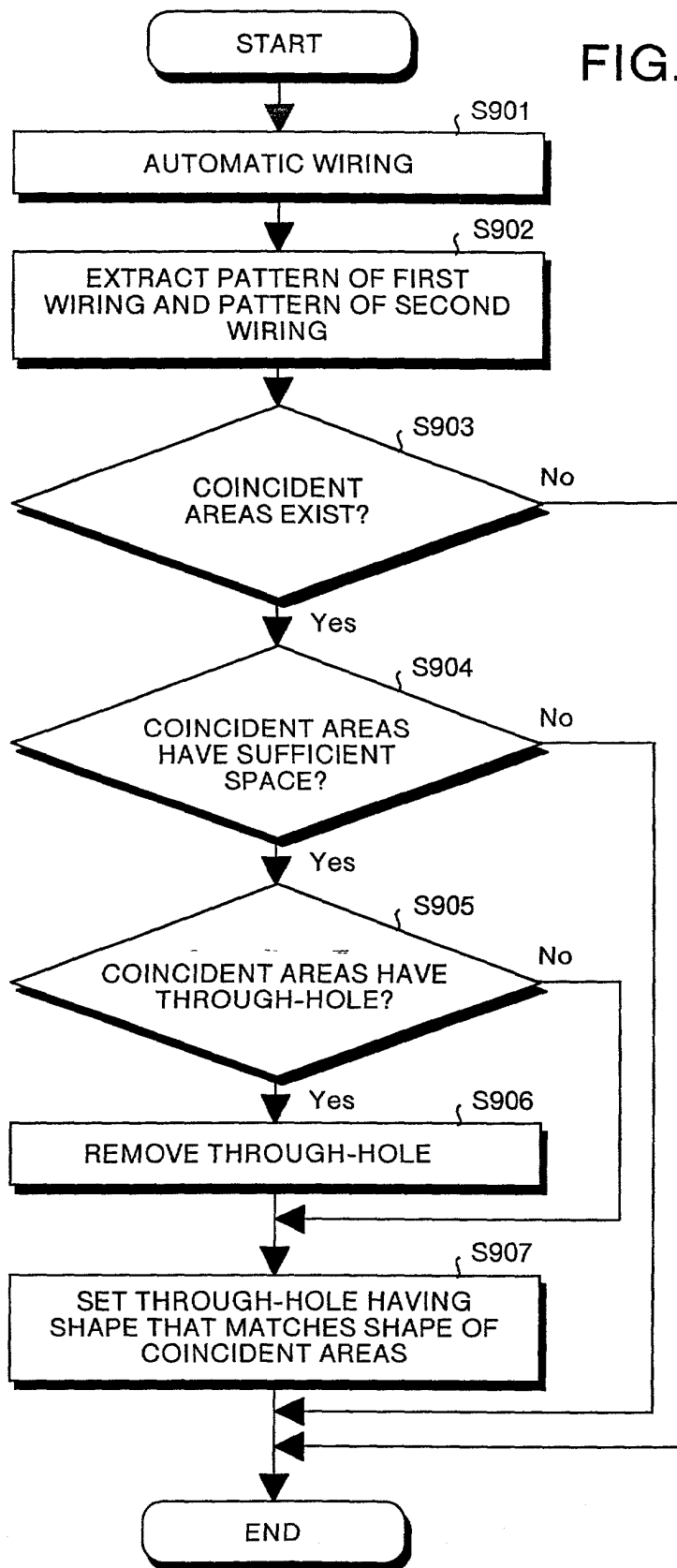


FIG.10



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